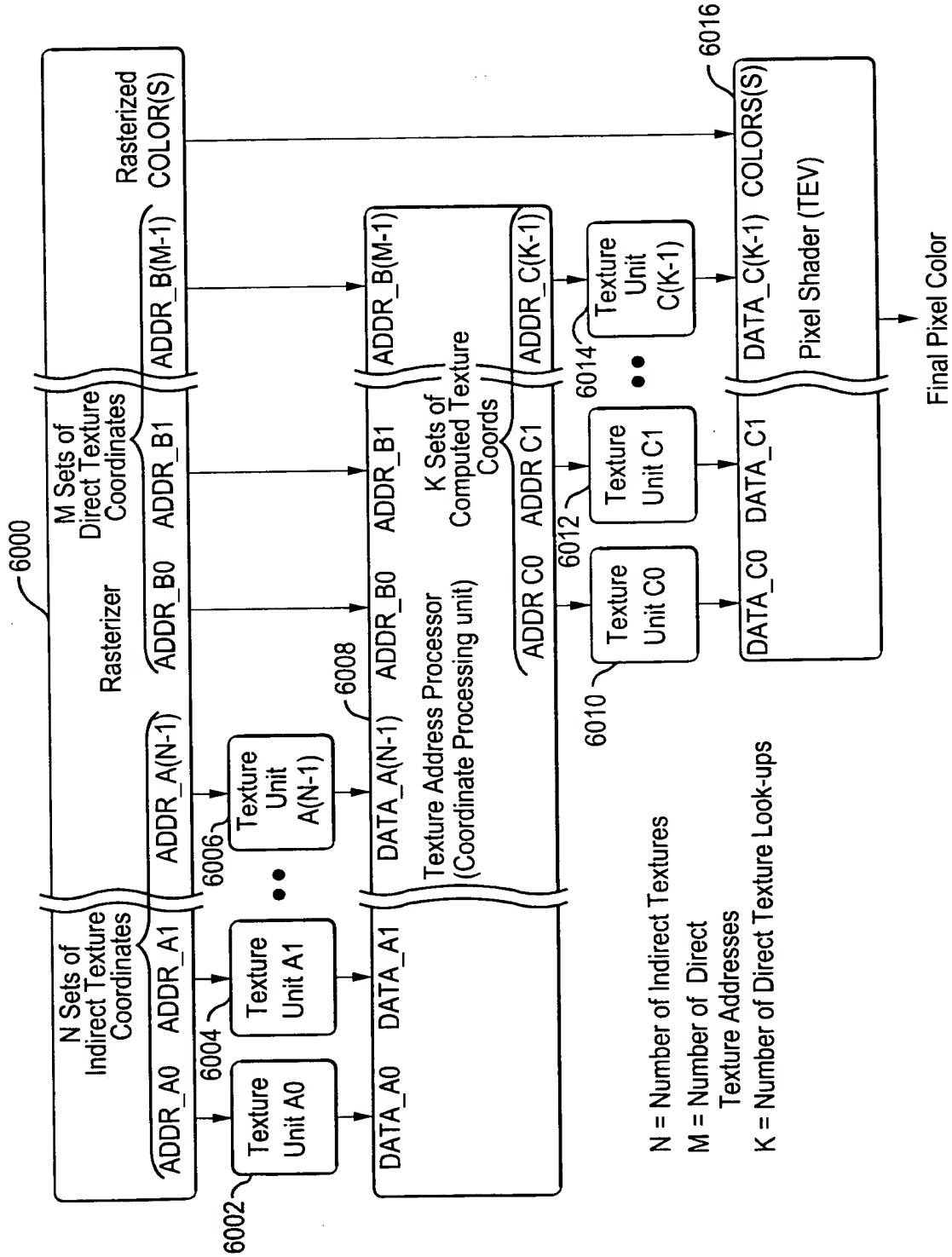


AMENDMENTS TO THE DRAWINGS

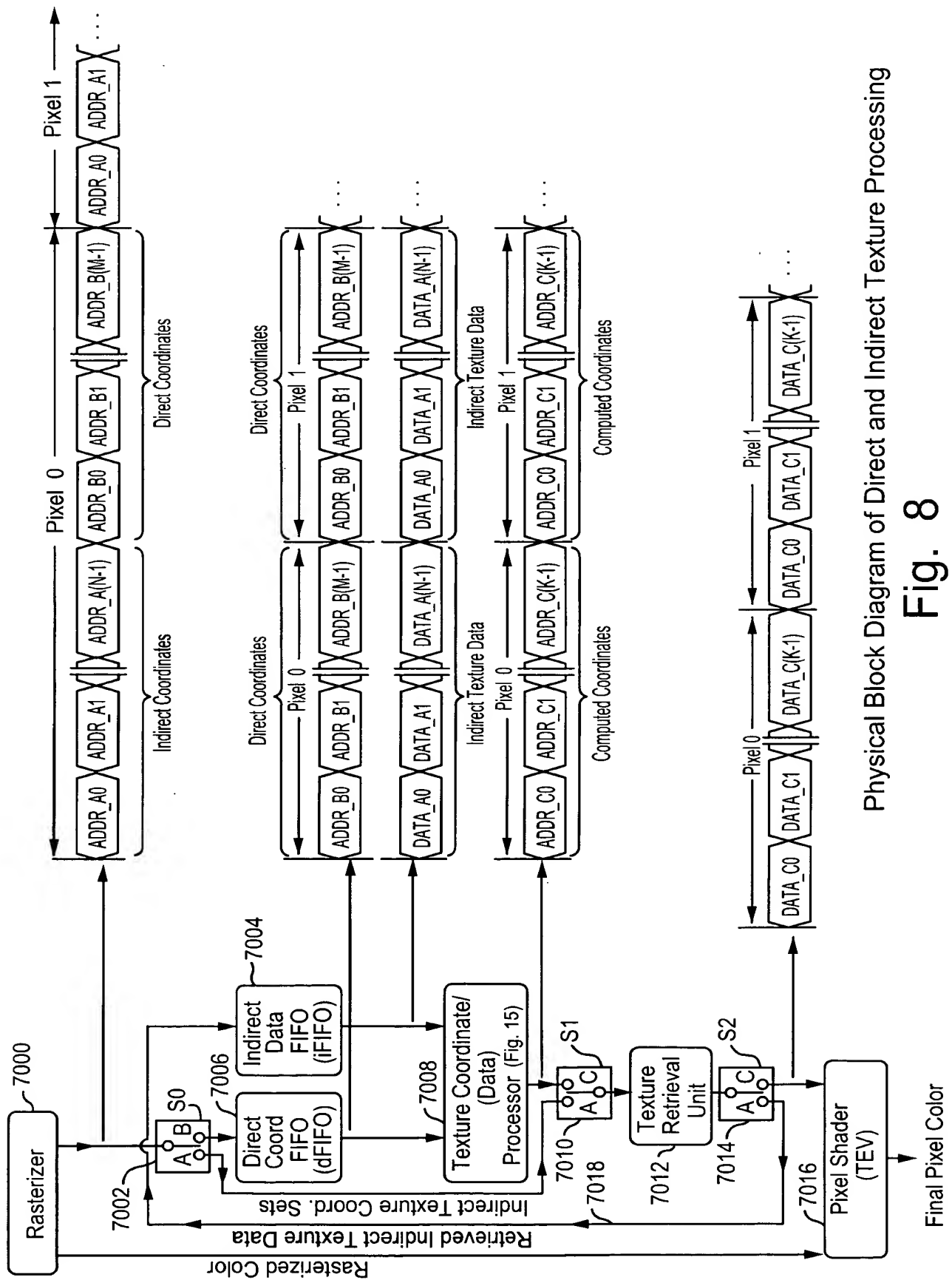
The attached sheets of drawings include annotated sheets including changes to Figures 6, 8, 10A-10K, 14, 15, 16A and 16B. Also included are replacement sheets for the original drawing sheets containing these Figures. Labels and headings were amended in the aforementioned Figures to correct typographical errors and to more clearly identify the Figures so as to correspond more closely with the written descriptions set forth in the specification. In Figure 6, the label on element 6008 is amended. In Figure 8, labels on elements 7008, 7012 and 7018 are amended. In Figures 10A-10K the label on element 7008 is amended. In Figure 14, the label on element 512 is amended. In Figure 15, an identifying label is added to the line out from Bias element 523. In Figures 16A and 16B, the figure headings are amended.

Attachment: Replacement Sheet(s)
Annotated Sheet(s) Showing Changes



Logical Block Diagram of Indirect Texture Processing

Fig. 6



Physical Block Diagram of Direct and Indirect Texture Processing

Fig. 8

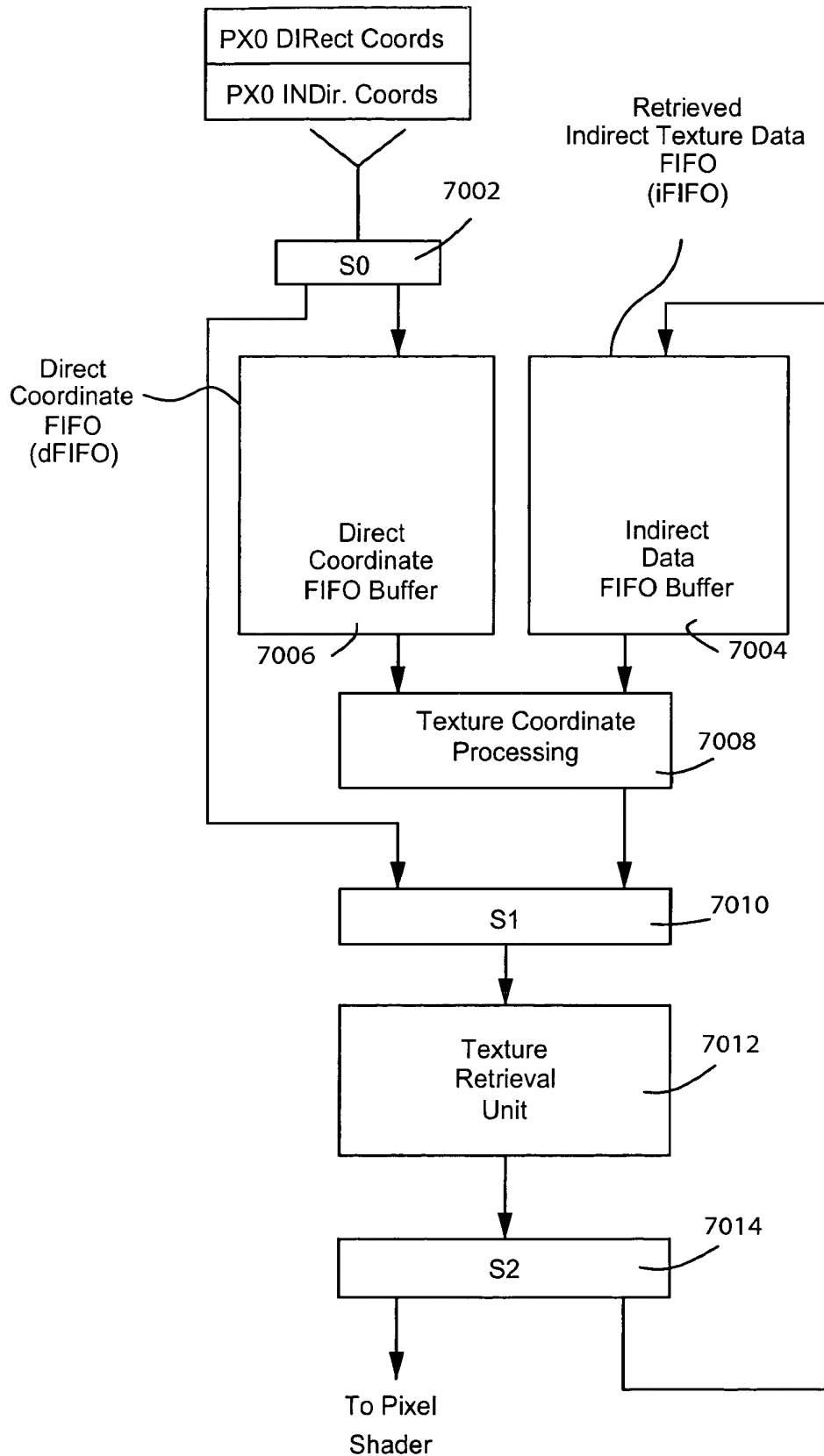


Fig. 10A

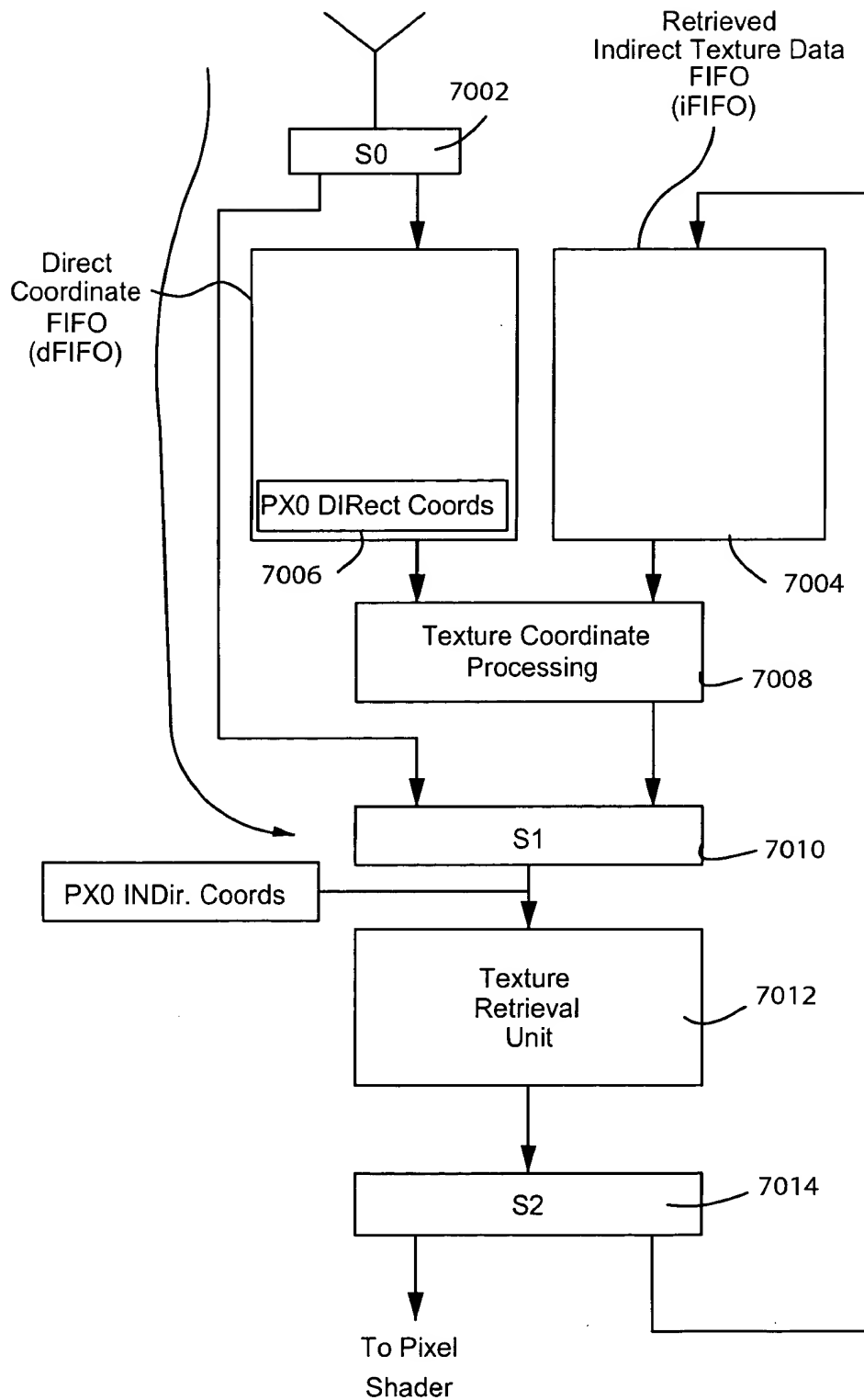


Fig. 10B

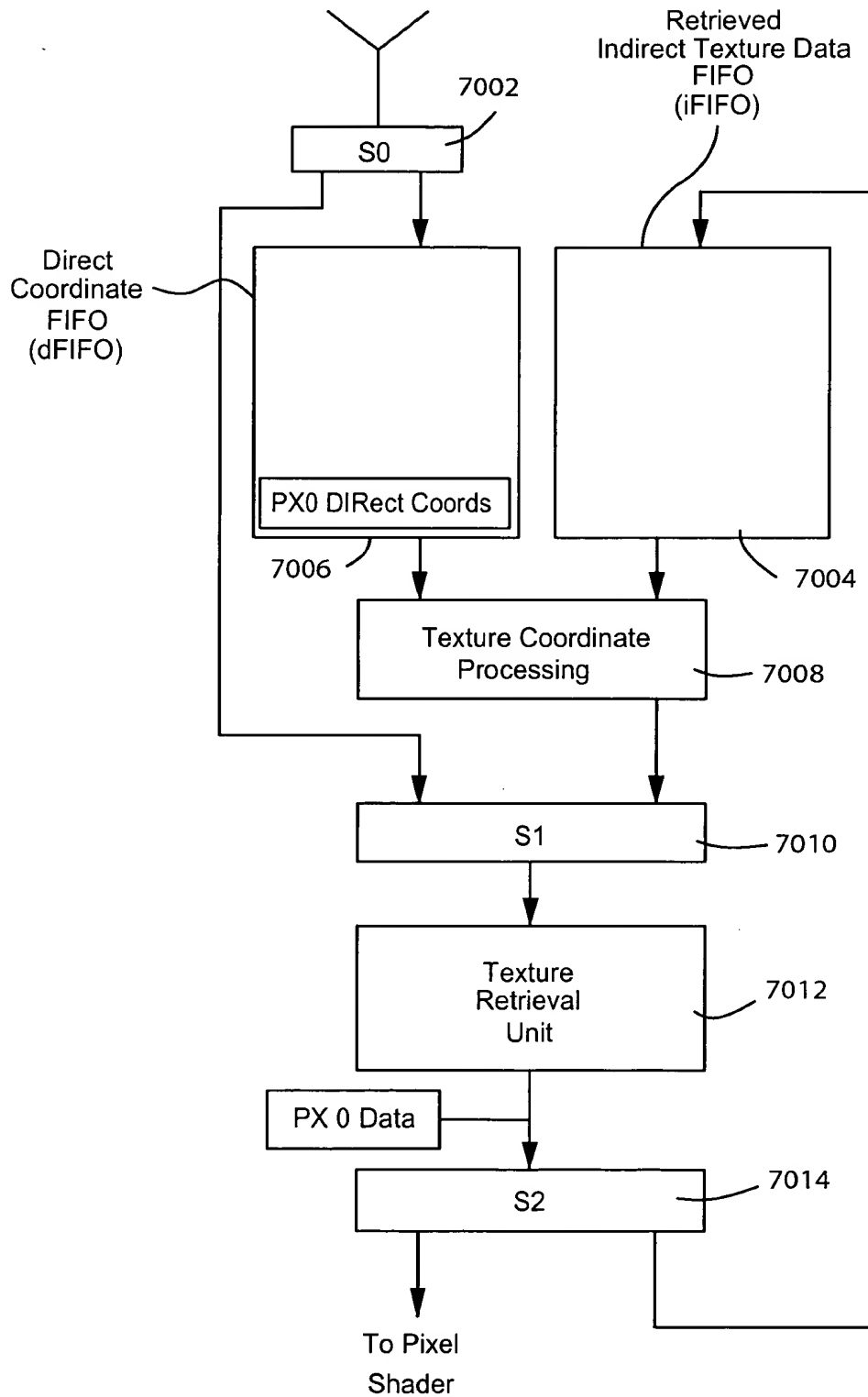


Fig. 10C

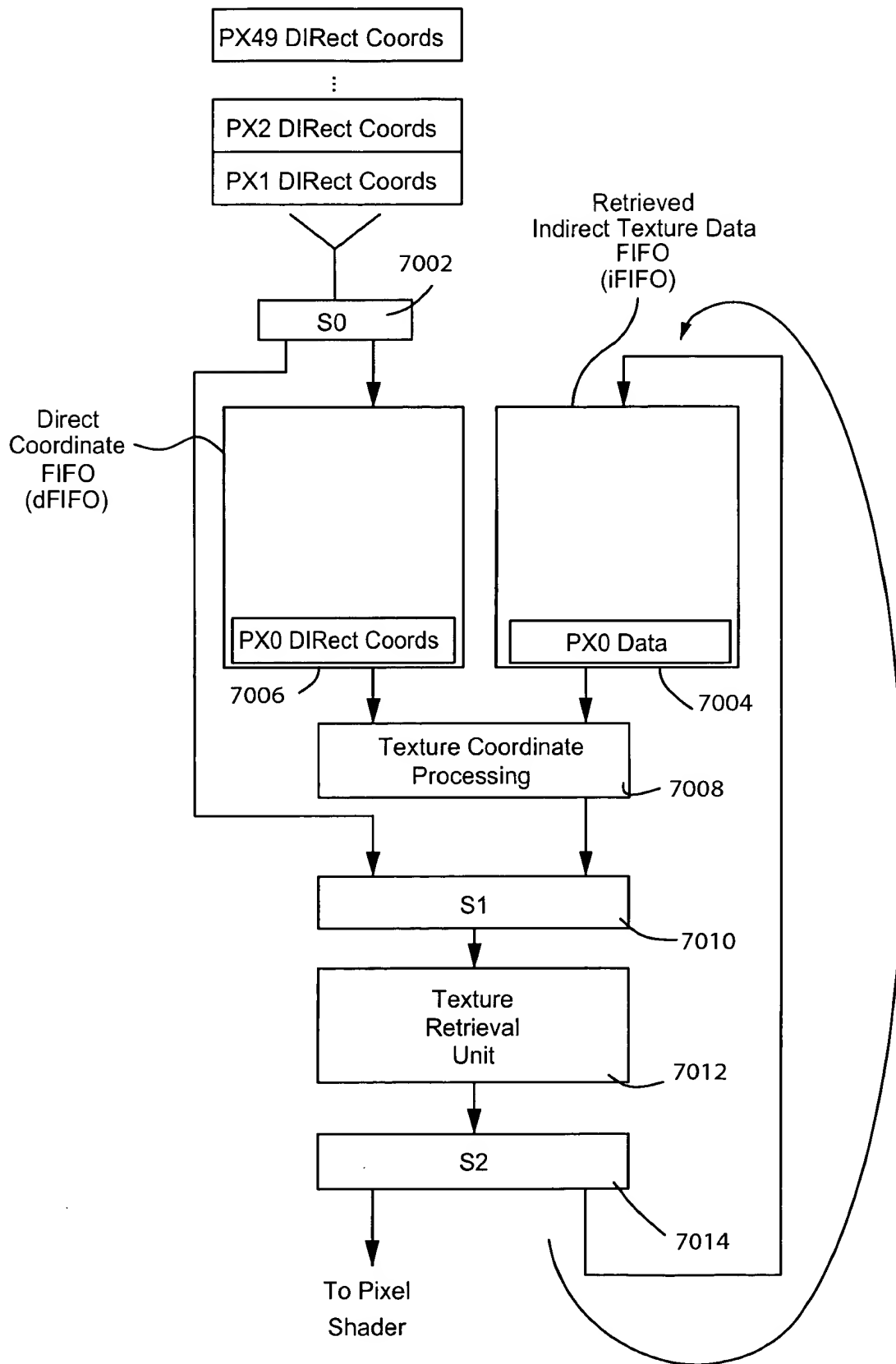


Fig. 10D

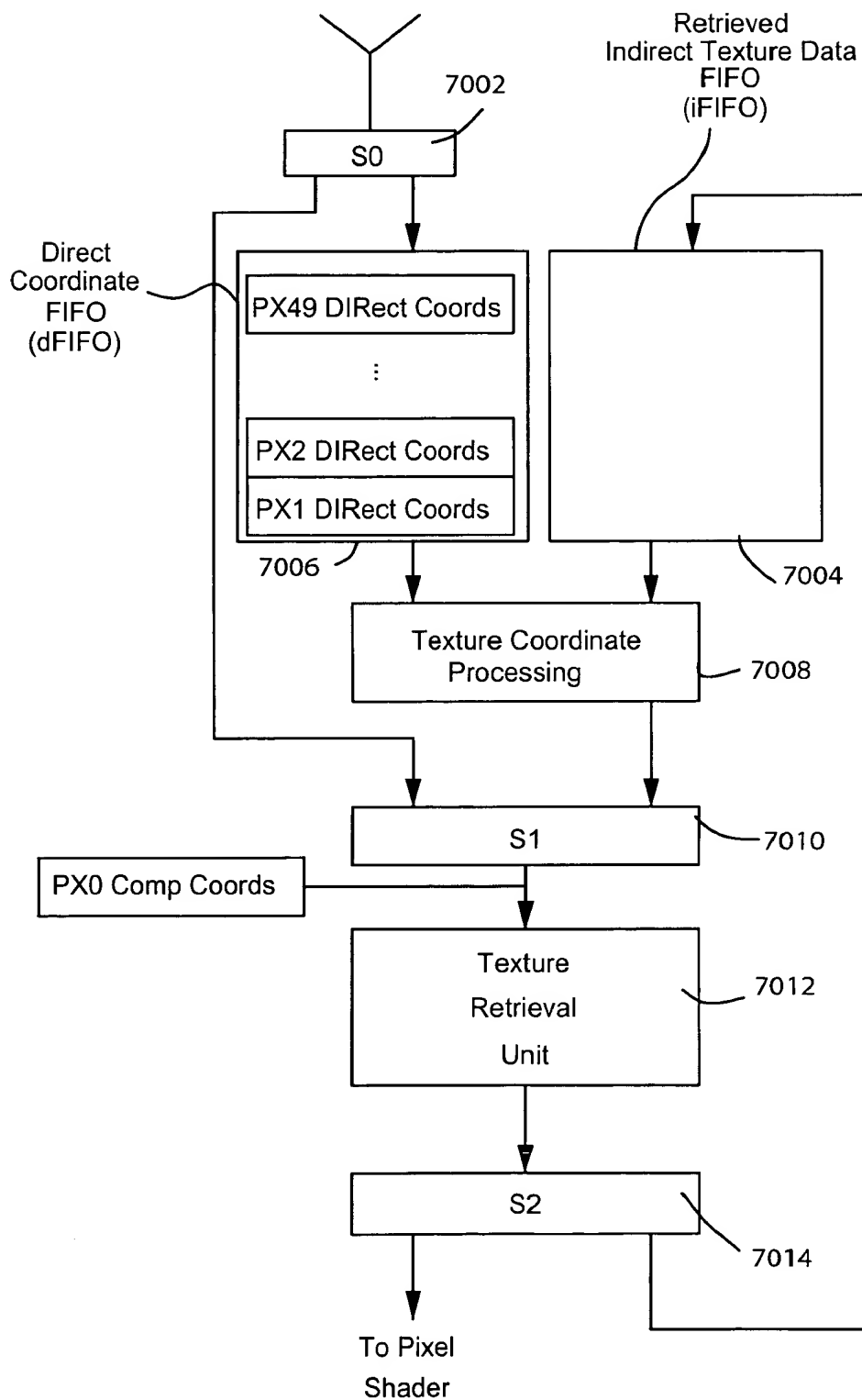


Fig. 10E

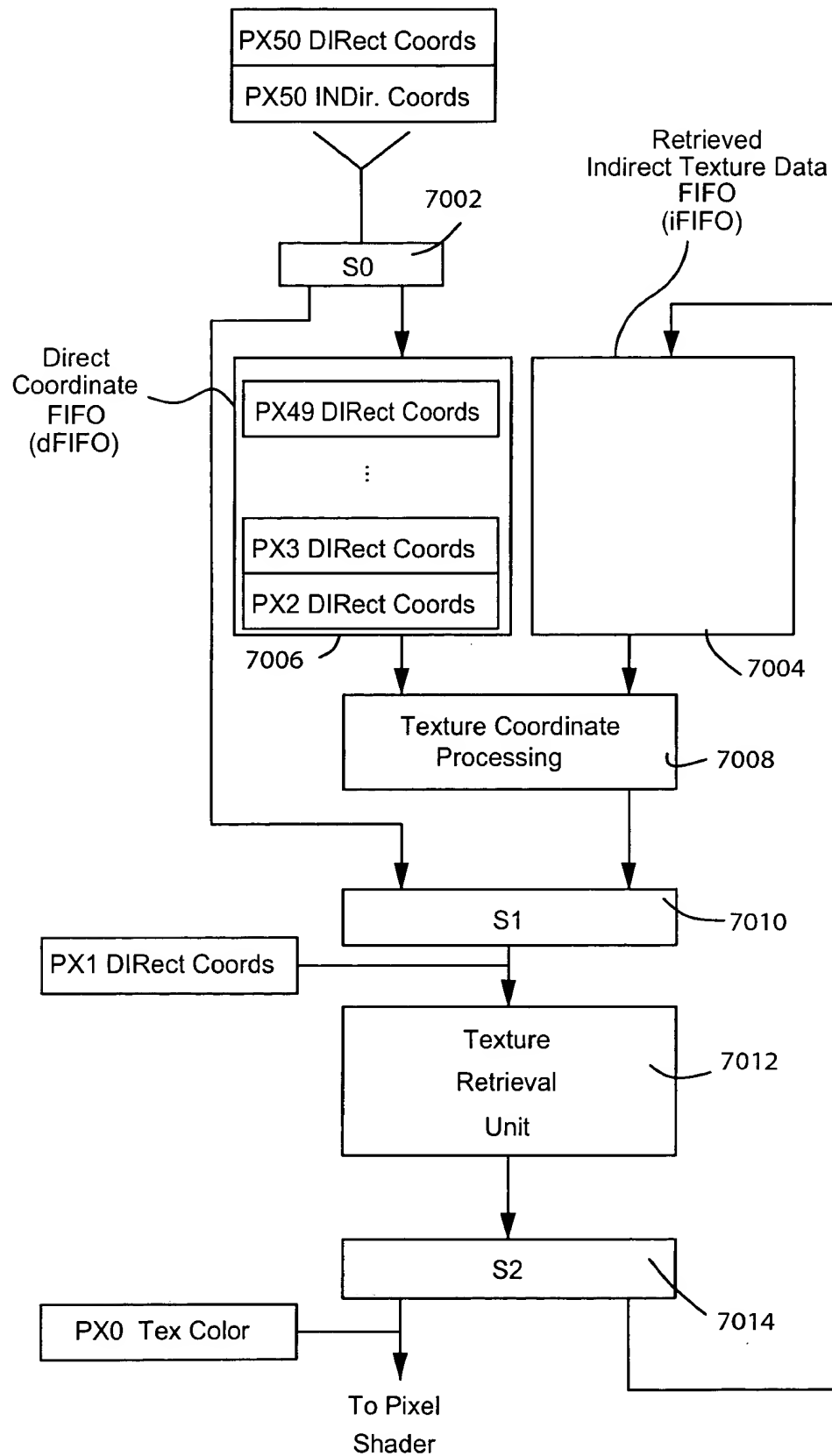


Fig. 10F

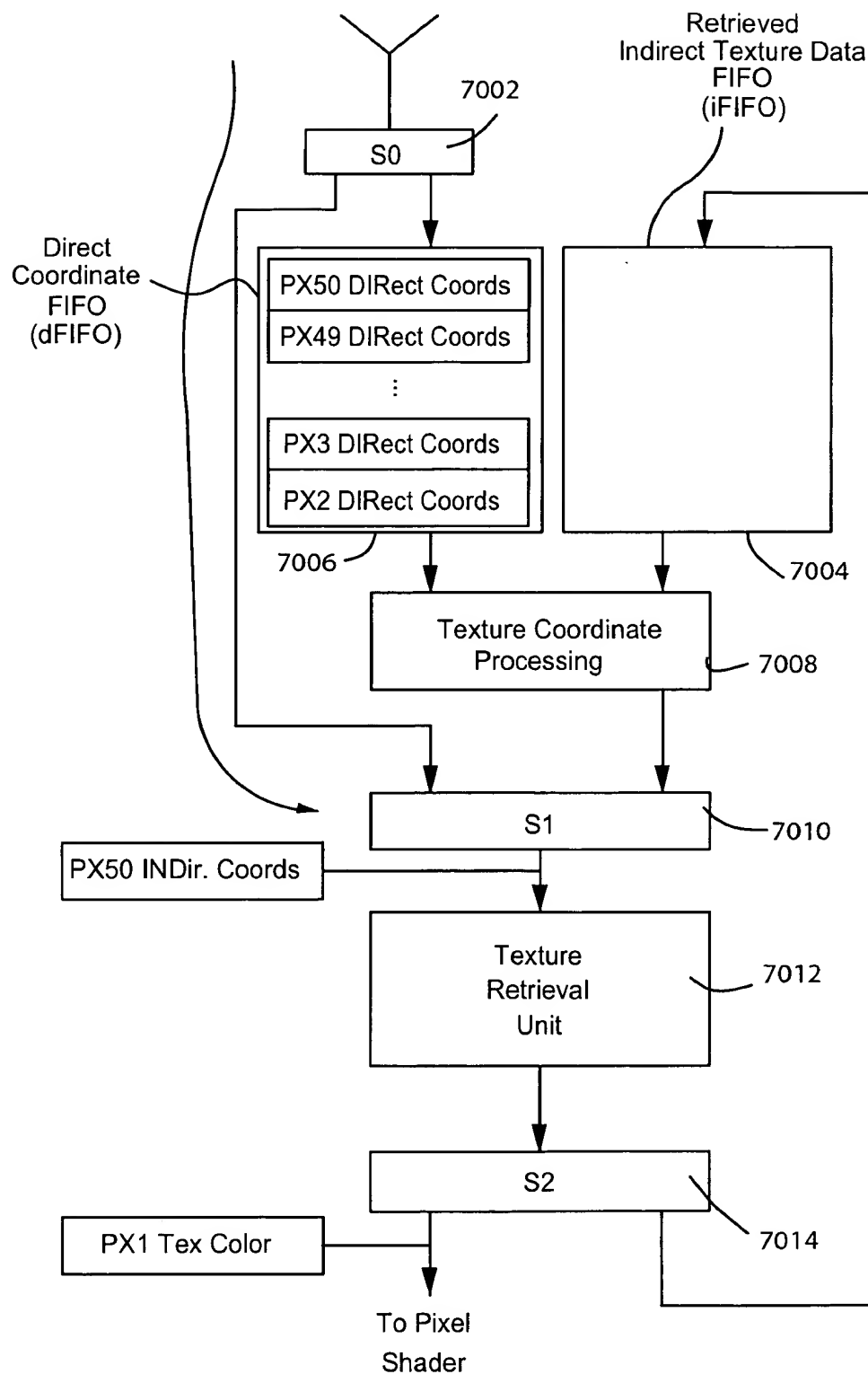


Fig. 10G

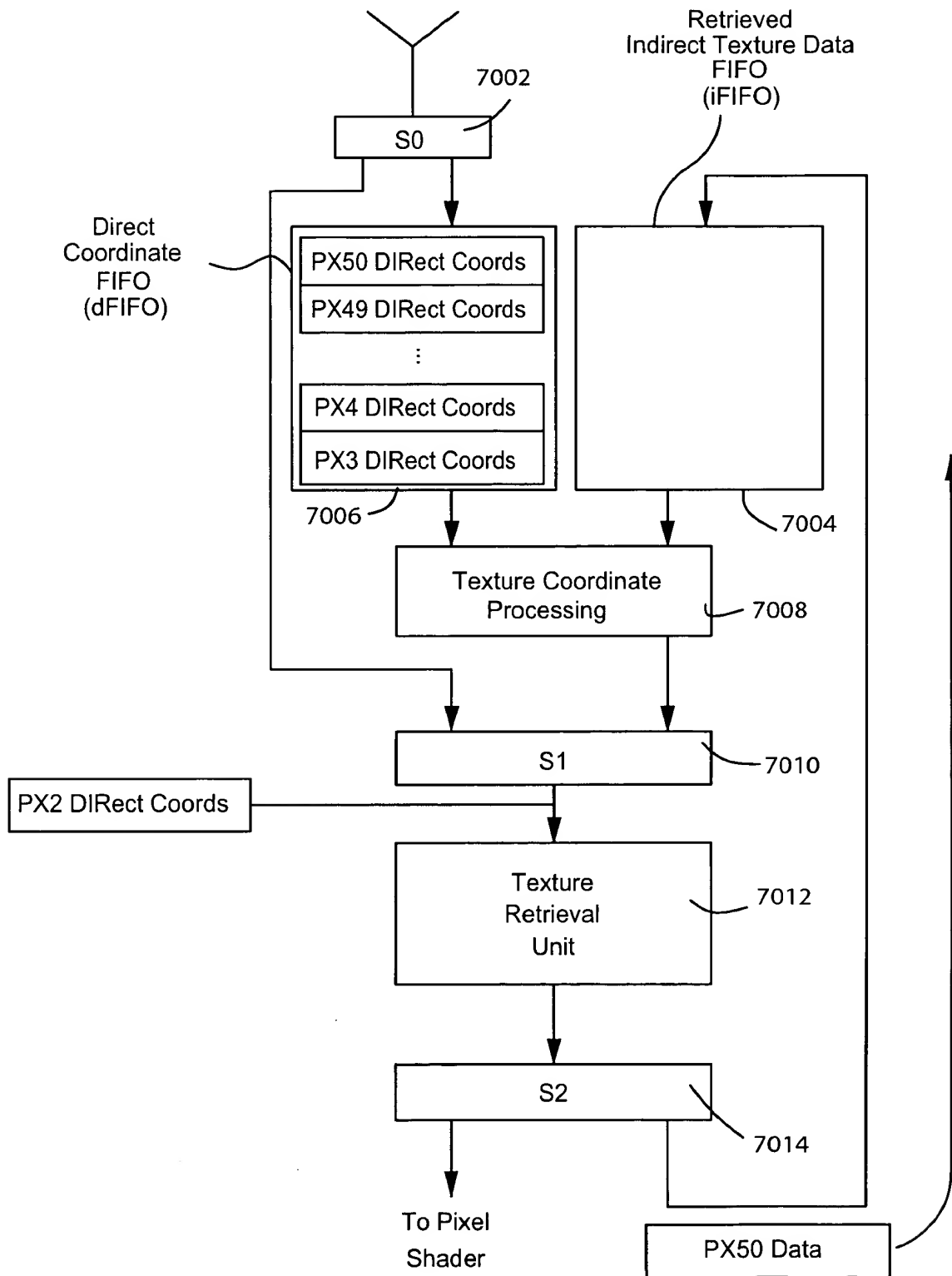


Fig. 10H

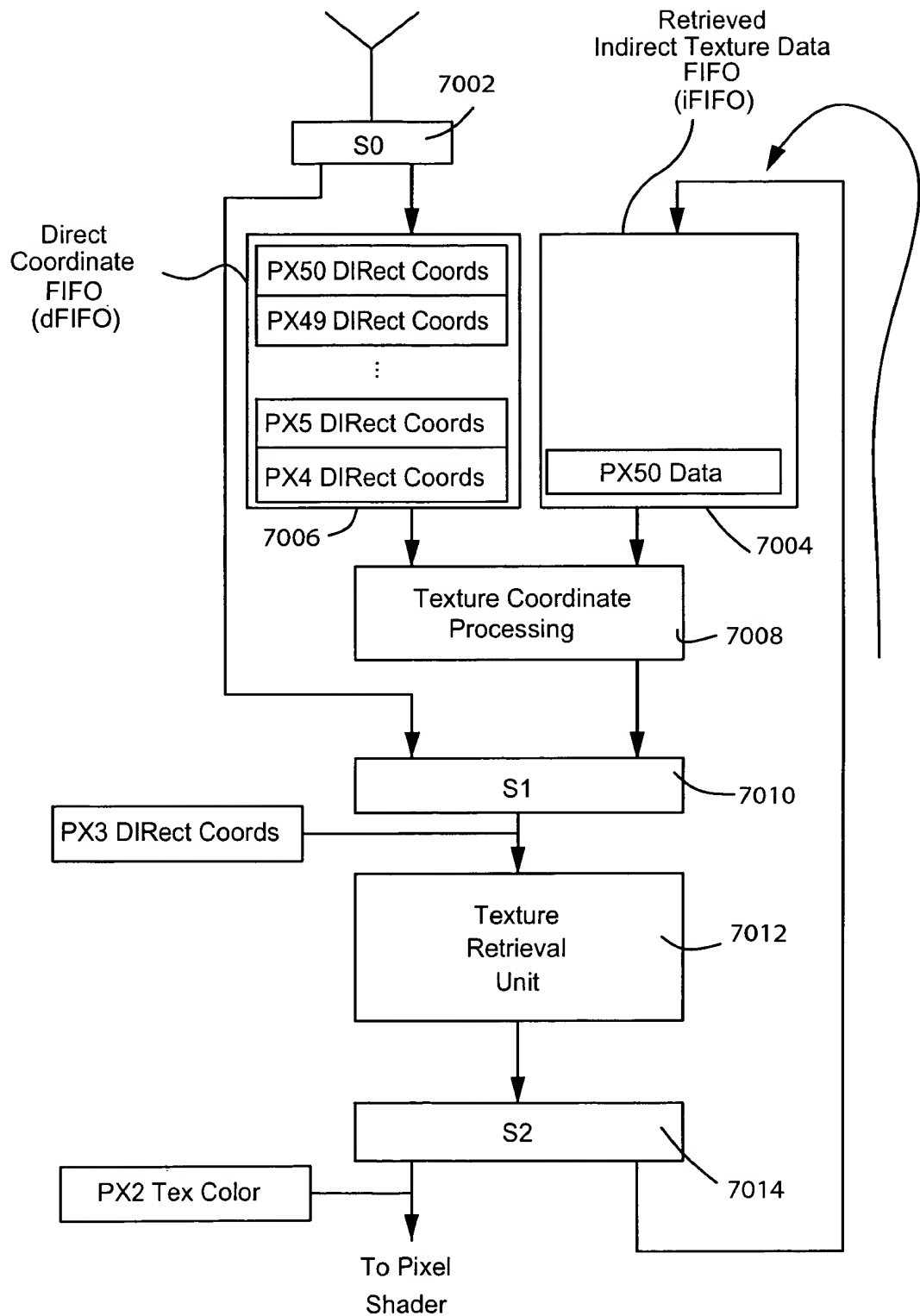


Fig. 10I

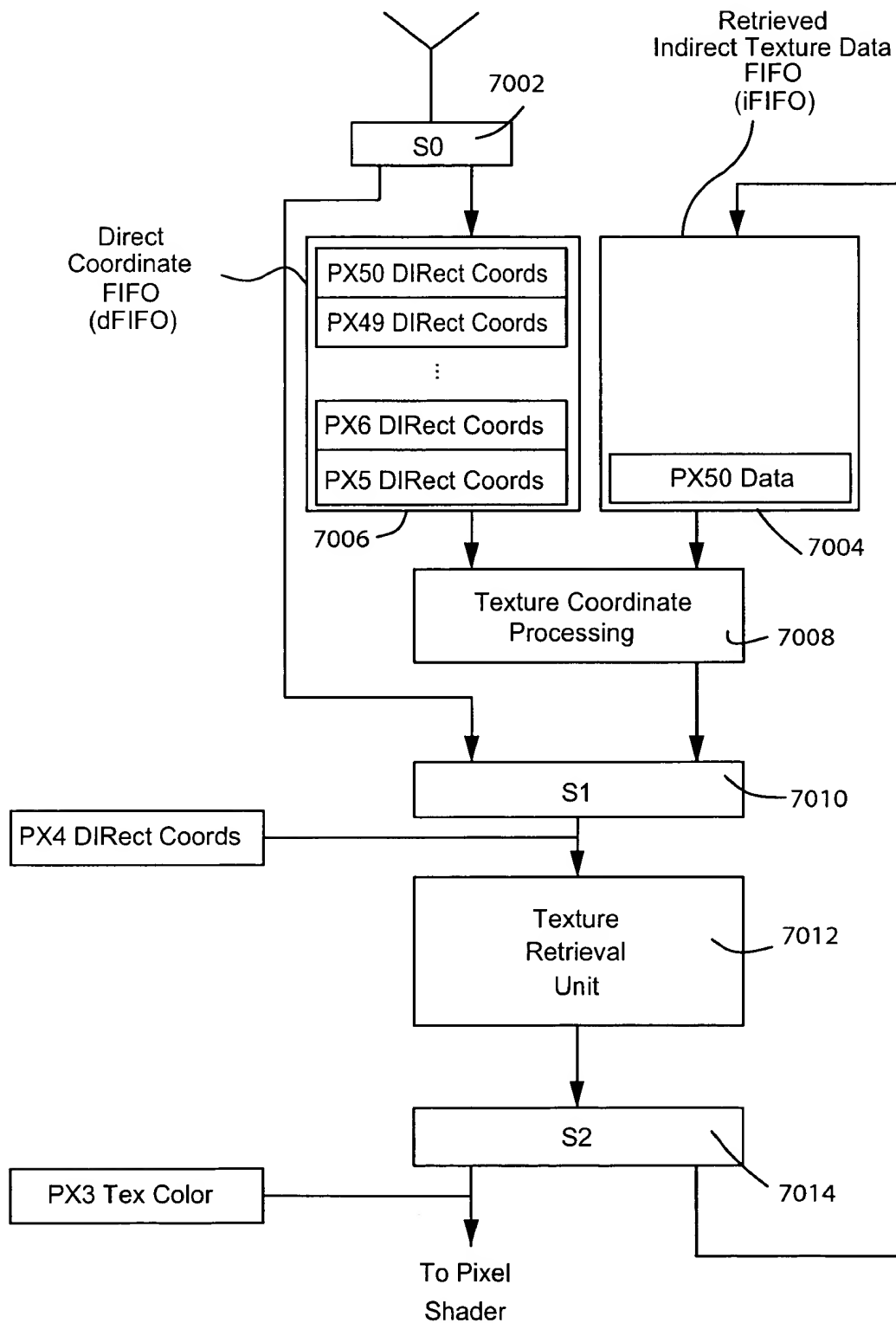


Fig. 10J

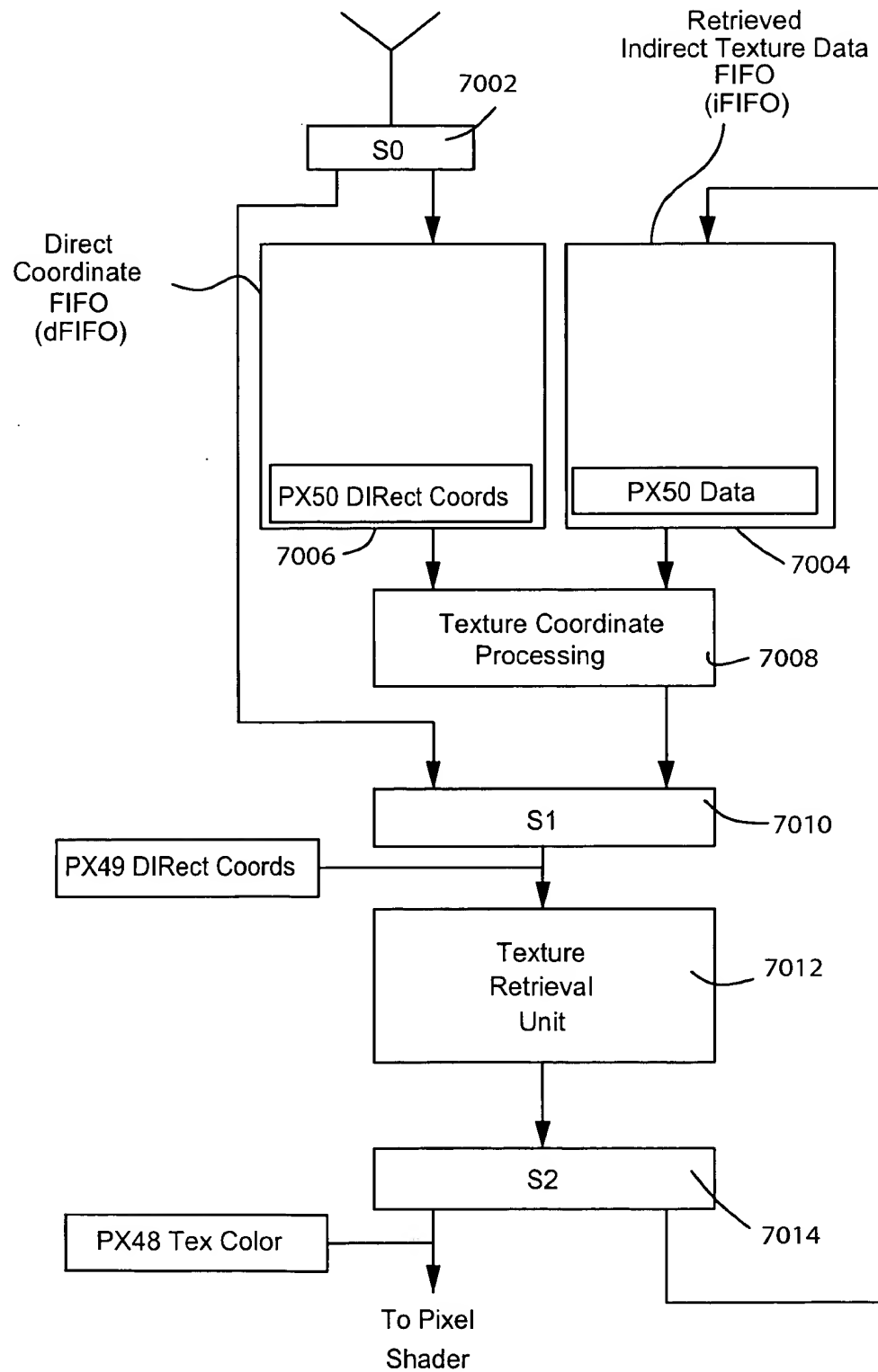


Fig. 10K

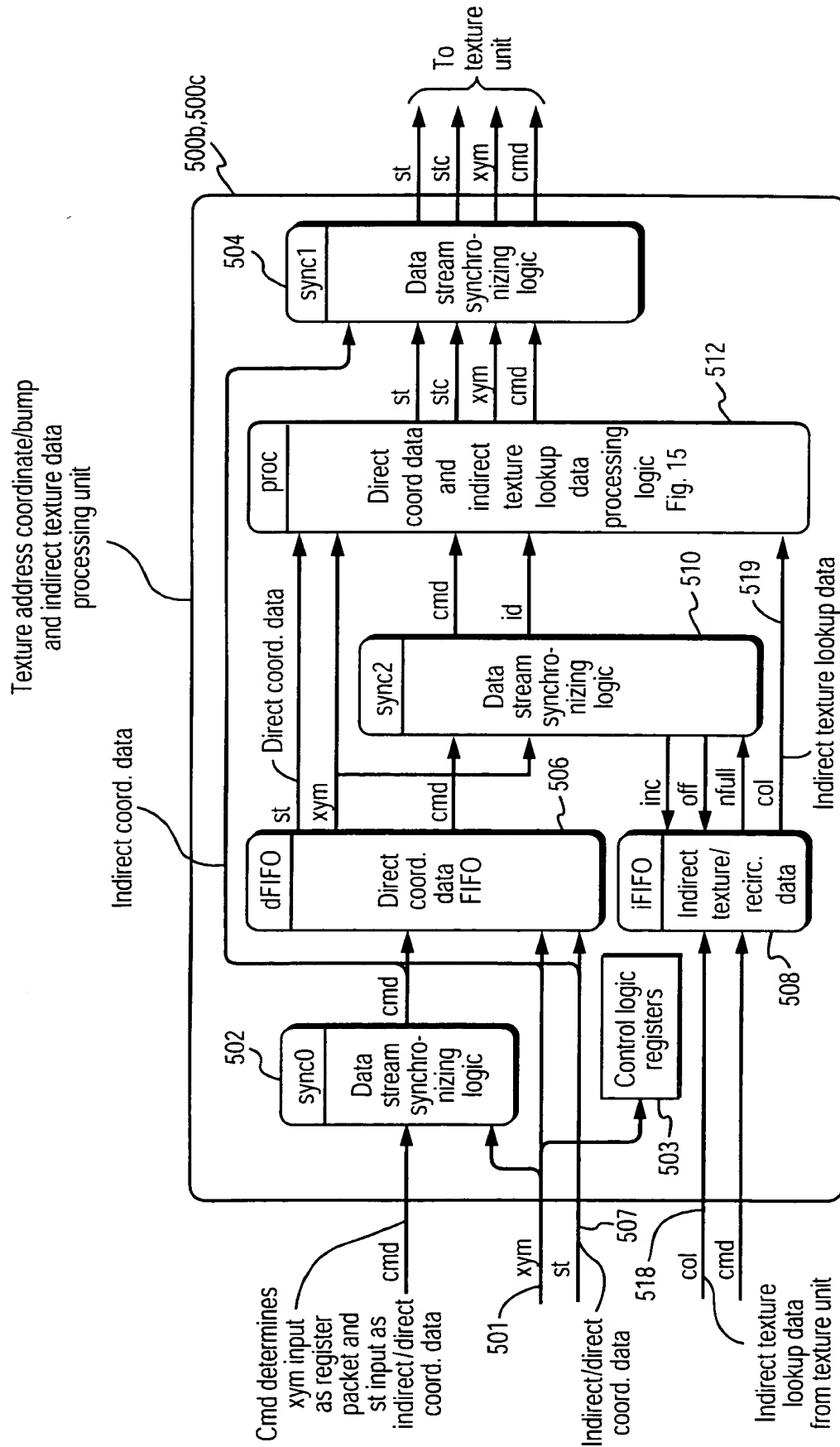


Fig. 14

EXAMPLE TEXTURE COORDINATE/BUMP
 PROCESSING UNIT

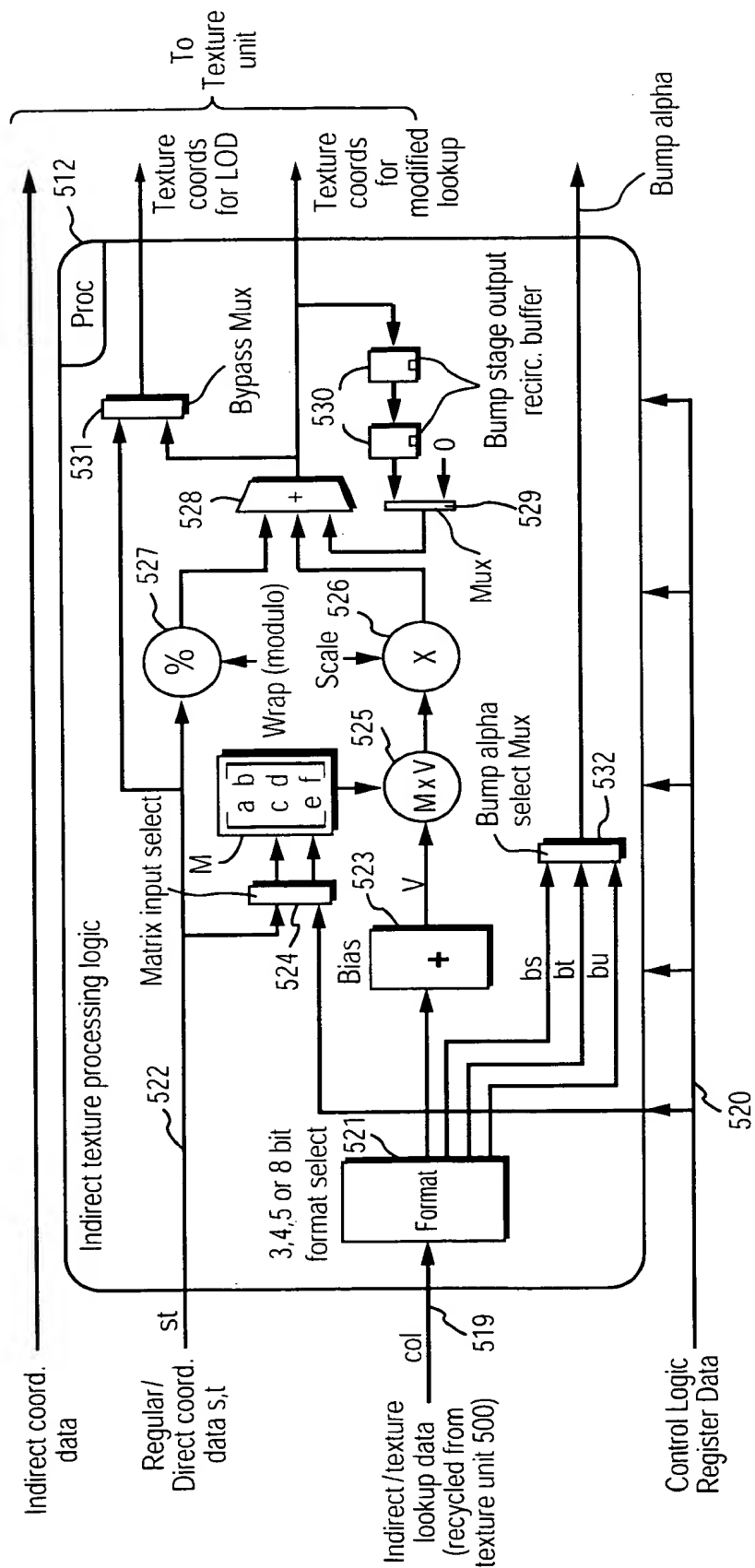


Fig. 15

EXAMPLE INDIRECT-TEXTURE
 LOOKUP DATA PROCESSING LOGIC

$$\begin{pmatrix} s' \\ t' \end{pmatrix} = \begin{pmatrix} ma & mb \\ mc & md \\ me & mf \end{pmatrix} \cdot \begin{pmatrix} s \\ t \\ u \end{pmatrix}$$

$\begin{matrix} M \\ V \end{matrix}$

Fig. 16A

EXAMPLE TEXTURE
STATIC OFFSET MATRIX

Matrix A

$$\begin{pmatrix} s/256 & t/256 \\ 0 & 0 \\ 0 & 0 \end{pmatrix}$$

Matrix B

$$\begin{pmatrix} 0 & 0 \\ s/256 & t/256 \\ 0 & 0 \end{pmatrix}$$

Fig. 16B

EXAMPLE TEXTURE
DYNAMIC OFFSET MATRICES





$MTXA_i$	$s_i (1:0)$	$mb_i (10:0)$				$ma_i (10:0)$			
$MTXB_i$	$s_i (3:2)$	$md_i (10:0)$				$mc_i (10:0)$			
$MTXC_i$	$s_i (5:4)$	$mf_i (10:0)$				$me_i (10:0)$			
CMD_i		fb_i		tw_i	sw_i	m_i		$bias_i$	fmt_i bt_i
	⋮								
							$imask (7:0)$		
GEN MODE			$nbmp$		$ntev$		$ntex$		

Fig. 17

EXAMPLE CONTROL
LOGIC REGISTERS